



Call for Papers

**Special Issue on
Advanced in High Performance, Algorithm, and
Framework for Future Computing**

The Journal of Supercomputing
Editor-in-Chief: Prof. Hamid R. Arabnia



Introduction

Recently one of the hot topics regarding on future generation computing and network is "Advances in High Performance, Algorithm, and Framework for Future Computing" which is focusing on the framework of the cloud system and advanced information technology in order to accomplish more eco-responsible behavior of people. The development of computing environments, such as the smart grid, peer-to-peer, personal mobile cloud, machine-to-machine, pervasive and ubiquitous computing, is changing our environment. We are no longer limited to a room or a desktop and email services can be accessed almost anywhere via smart phones. However, such dynamic changes of these days, including the use of wireless, mobile communication technologies in future computing environments, bring a variety of future computing issues including converged security, high performance, advanced algorithm, and integrated framework.

The special issue aims at bringing together researchers and practitioners who are interested in both computing and the possible impact of computing on the environment, and who are committed to work on the development of eco-effective technologies and technology applications.

Topics of primary interest include, but are not limited to:

- Cloud, Cluster, and Green IT for Future Computing
- Parallel and distributed processing for Future Computing
- Security, Privacy and Trust Issues in Future Computing
- Platform and Infrastructure in Future Computing
- Grid Network its Services and Applications for Future Computing
- Human centric and Social Network services
- Innovative use of hardware and software unconventional for future computing
- High performance applications or visualizations in connection with GPUs (GPGPU)
- Low power/embedded processors, FPGAs, Intel's new Knights Corner and SCC
- Research processor, Tiler's tile-based many-core processors, IBM Cell BE, concepts for AMD Fusion, accelerators, visualization cards, etc.

Submission Details:

Original, high quality contributions that are not yet published or that are not currently under review by other journals or peer-reviewed conferences are sought. Submitting authors should follow the Author Guidelines available from <http://www.springer.com/computer/programming/journal/11227>. Paper submissions must follow the instructions described in the Journal of Supercomputing Web site at <http://www.editorialmanager.com/supe/> Authors have to select Choose Article Type "**Sp Iss: Advanced HAF-FutureCom**".

Important Dates:

Submission due: Aug. 1, 2011
Final versions due: Nov. 1, 2011

Acceptance Notification: Oct. 1, 2011
Publication date: 4Q, 2012 (Tentative)

Guest Editors:

Prof. Taeshik Shon (Corresponding GE)
Corresponding Editor
Division of Information and Computer Engineering
Ajou University, South Korea
Email: Taeshik.fra@gmail.com

Prof. Shih-Jeng Wang
Department of Information Management
Central Police University, Taiwan
Email: sjwang@mail.cpu.edu.tw

Prof. Liudong Xing
Department of Electrical and Computer
Engineering
University of Massachusetts, USA
Email: lxing@umassd.edu